

L Number	Hits	Search Text	DB	Time stamp
6	116	(round?robin) near4 (buffer)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/29 09:57
7	5	(round?robin) near4 (buffer) near4 schedul\$3	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/29 09:57
8	0	(tree adj1 engine) near4 pars\$3 near4 (data near4 protocol)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/29 11:17
9	0	(tree) near4 pars\$3 near4 (data near4 protocol)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/29 11:18
10	1	(tree adj1 engine\$1) near4 (pars\$3 near4 data)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/29 11:18
11	1	(tree adj1 engine\$1) with (pars\$3 near4 data)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/29 11:18
12	1	(tree adj1 engine\$1) same (pars\$3 near4 data)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/29 11:19
13	145	(pars\$3 near4 data) near4 protocol	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/29 11:20
14	0	(pars\$3 near4 data) near4 protocol near4 thread\$3	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/29 11:21
15	0	(pars\$3 near4 data) near4 protocol near4 (RAM and cache)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/29 11:22
16	20	(pars\$3 adj1 data) near4 protocol	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/29 11:35
17	24	pars\$3 with tree with protocol	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/29 11:36
-	174	(712/206).CCLS.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 15:25
-	446	(712/215).CCLS.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 15:25
-	478	(712/228).CCLS.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 15:34

-	4	switch\$3 near4 thread\$3 near4 (long?latency adj1 instruction\$1)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 15:46
-	4	(switch\$3 near4 thread\$3) with (long?latency adj1 instruction\$1)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 16:02
-	4	(switch\$3 near4 thread\$3) with (long adj1 latency adj1 instruction\$1)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 16:09
-	6	(switch\$3 near4 thread\$3) same (long adj1 latency adj1 instruction\$1)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 16:11
-	2	(context adj1 switch\$3) same (long adj1 latency adj1 instruction\$1)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 16:12
-	2	(context adj1 switch\$3) same ((long adj1 latency) near4 instruction\$1)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 16:22
-	1	(context adj1 switch\$3) with ((long adj1 latency) near4 instruction\$1)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 16:22
-	3	(context adj1 switch\$3) same (prevent\$3 near4 thread\$3 near4 execut\$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 16:23
-	5	(switch\$3 near4 thread\$3) same (prevent\$3 near4 thread\$3 near4 execut\$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 16:24
-	11	(switch\$3 near4 thread\$3) same (prevent\$3 near4 execut\$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 16:25
-	70	(switch\$3 near4 thread\$3) same (long near4 latenc\$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 16:26
-	39	(switch\$3 near4 thread\$3) near4 (long near4 latenc\$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 16:26
-	1351	switch\$3 near4 FIFO	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/28 14:07
-	1	(thread near4 switch\$3) near4 FIFO	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/28 14:08
-	0	(thread near4 switch\$3) with (pipeline near4 FIFO)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/28 14:08
-	0	(thread near4 switch\$3) same (pipeline near4 FIFO)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/28 14:08

-	106	(thread near4 switch\$3) same pipeline\$1	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/28 14:11
-	54	(thread near4 switch\$3) with pipeline\$1	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/28 14:12
-	3	(thread near4 switch\$3) near4 (round adj1 robin)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/28 14:18
-	9	(thread near4 switch\$3) with (round adj1 robin)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/28 14:19